

 Active

D8s | USPAT: US-PGFE

7 Purple

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29 and (cobalt near (contact or opening))

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	U	I	Document ID	Issue Date	Pages	Title	Current OR	Current ERef	Retrieval C	Inventor	S	C	P	J	%	In-
1	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 20020162047 A1	20021205	37	Method of manufacturing semiconductor device	438/622			Yokoyama, Natsuki et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 20
2	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 20020123216 A1	20020905		METHOD OF MANUFACTURING SEMICONDUCTOR DEVICE	438/622			Yokoyama, Natsuki et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	
3	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	US 20020019127 A1	20020214	15	Interconnect structure and method of making	438/637	438/618; 438/648;		Givens, John H.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 20
4	<input type="checkbox"/>	<input type="checkbox"/>	US 6461957 B1	20021008	35	Method of manufacturing semiconductor device	438/622	438/630; 438/648;		Yokoyama, Natsuki et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US
5	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 6271122 B1	20010807	7	Method of compensating for material loss in a metal sil	438/627	257/E21.165; 257/E21.584;		Wieczorek, Karsten et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US

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